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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

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| Sheet | 1 | of | 2 |
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| Application Number | 10/815,370 |
| Filing Date | 01 April 2004 |
| First Named Inventor | CHIH C. YEH |
| Art Unit | 2827 |
| Examiner Name | TRONG Q. PHAN |
| Attorney Docket Number | MXIC 1571-1 |

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

Trouy Phan

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| Date | |
| Considered | |

5/14/06

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| | | Examiner Name | Trong Q. Phan |
| Sheet 2 | of 2 | Attorney Docket Number | MXIC 1571-1 |

| NON PATENT LITERATURE DOCUMENTS | | | |
|---------------------------------|-----------------------|---|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| TP | | SWIFT, C.T., et al. "An Embedded 90nm SONOS Nonvolatile Memory ..." IEDM 2002, 8-11 DEC 2002, 927-930. | |
| TP | | SHE, MIN, et. al., "Silicon-Nitride as a Tunnel Dielectric..." IEEE Electron Device Letters 24(5), May 2003, 3 pages. | |
| TP | | KIM, BYUNCHEUL, et al., "A scaled SONOS Single-Transistor..." J. Korean Phys. Soc. 41(6), Dec. 2002, 945-948. | |
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| Examiner Signature | Trong Q. Phan | Date Considered | 5/14/06 |
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